MIPS Datapath

Datapath

PC
Input

Output

Instruction Memory
Input

Output

Re	:	-1	 - 1	-:1	ı
R F	201			- 11	16
110	- 51	2			. •

Inputs Read register 1 Read register 2 Write register Write data Control Line: Reg Write Outputs Read data 1 Read data 2

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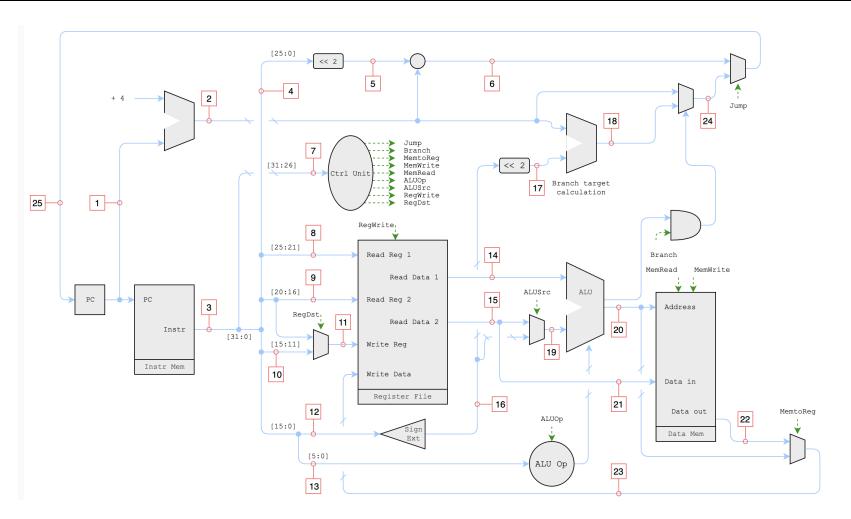
Computer Systems and Assembly

MIPS Datapath

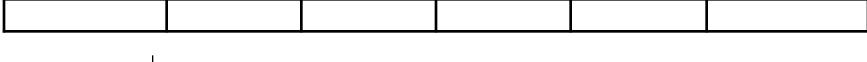
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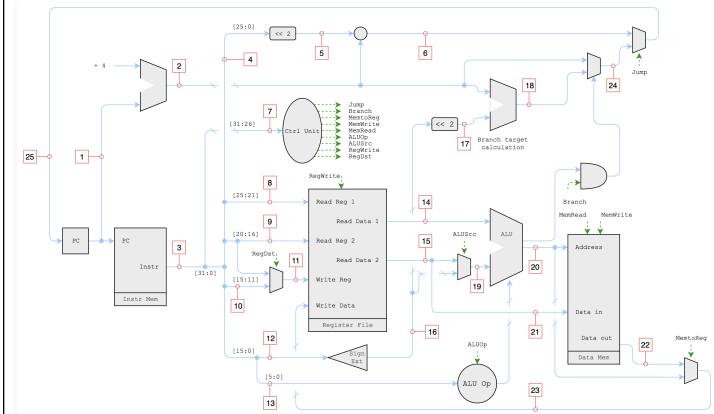
R Type





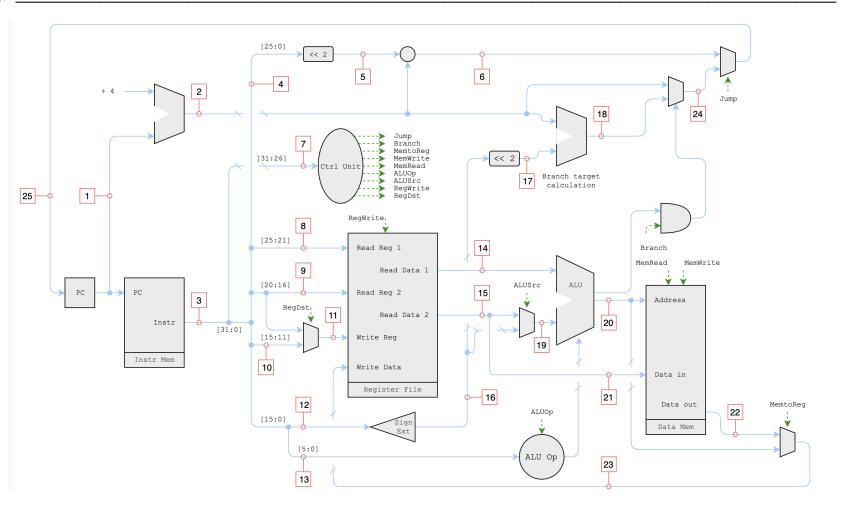
Arithmetic Example:



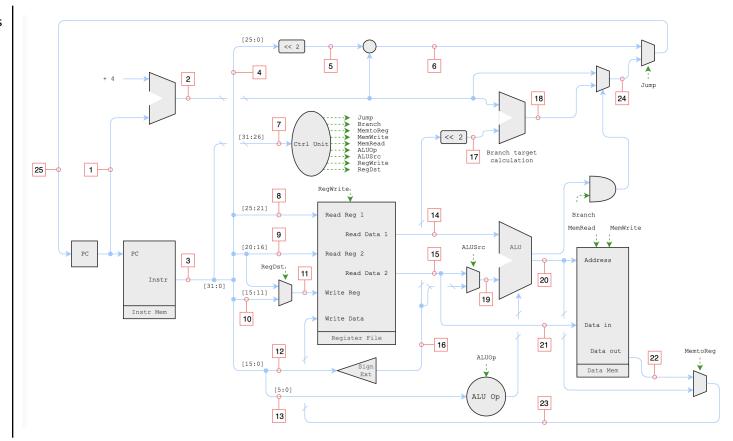


I Type

e.g.

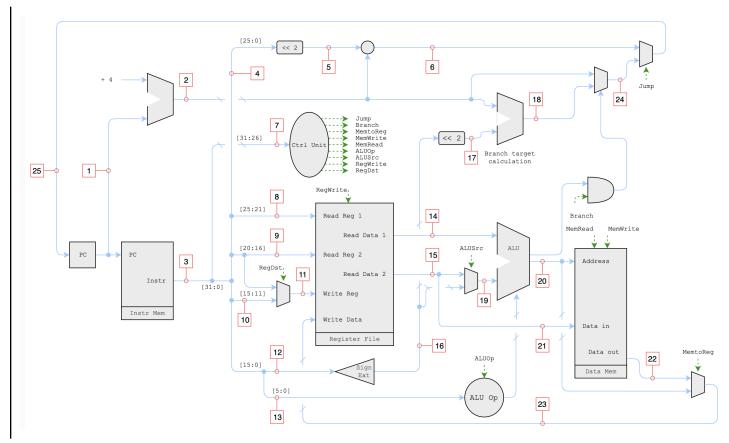


Arithmetic Example:

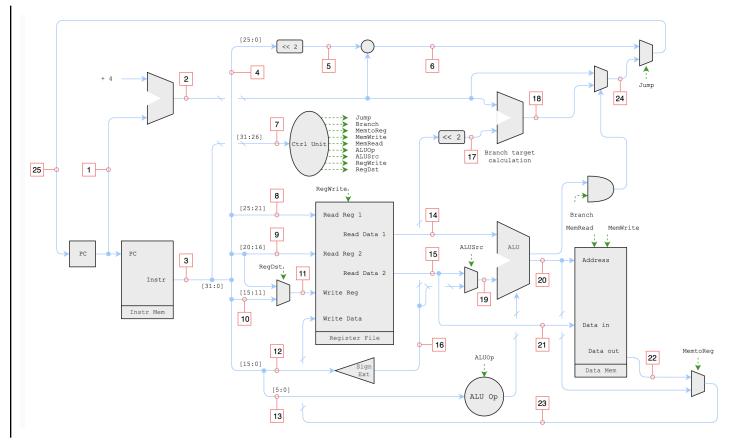


Load	Exam	ple:

oud Example:					

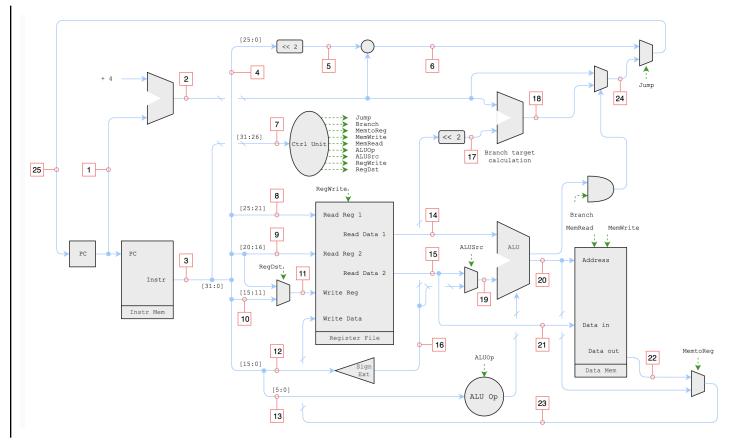


Store Example:



Branc	h Exam	iple:

Fullett Example:					



J Type

